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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/669,589	09/23/2003	Robert O. Conn	X-465 US	5691
24309	7590	09/17/2004	EXAMINER	
XILINX, INC ATTN: LEGAL DEPARTMENT 2100 LOGIC DR SAN JOSE, CA 95124			LE, DON P	
			ART UNIT	PAPER NUMBER
			2819	

DATE MAILED: 09/17/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

**Office Action Summary**

Application No.

10/669,589

Applicant(s)

CONN ET AL.

Examiner

Don P Le

Art Unit

2819

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 23 September 2003.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-25 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-4, 11, 16-20, 22 is/are rejected.
- 7) ☐ Claim(s) 5-10, 12-15, 21 and 23-25 is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- |  |   |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)  | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                                   | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)             |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)<br>Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____  |

*Claim Rejections - 35 USC § 102*

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1, 2, 11, 17-20 and 22 are rejected under 35 U.S.C. 102(b) as being anticipated by Cruz et al. (US 6,437,653).

3. With respect to claim 1, figures 1-6 of Cruz teach an integrated circuit comprising:

a clock distribution network (figure 1); and

an inductance control circuit (206, 302, 304) coupled to the clock distribution network and receiving a clock signal (104), wherein the inductance control circuit, comprising an inductance, is configured to provide a resonant frequency of the clock distribution network as a function of a clock frequency of the clock signal.

4. With respect to claim 2, figure 3 of Cruz discloses the inductance provided by the inductance control circuit is adjustable.

5. With respect to claim 11, figure 3 of Cruz discloses a capacitance control circuit (308) coupled between the clock distribution network and ground.

6. With respect to claim 17, it is inherent that the function is equivalent.

7. With respect to claims 18-20 and 22, the methods therein are inherent in the apparatus of Cruz as shown in the above rejections.

**CLAIMS 1 AND 2 ARE REJECTED AGAIN WITH A DIFFERENT  
REFERENCE FOR THE PURPOSE OF REJECTING DEPENDENT CLAIM**

**3.**

8. Claims 1, 3, 11, 16 and 18-20 are rejected under 35 U.S.C. 102(b) as being anticipated by Lamatsch (US 5,627,482).

9. With respect to claim 1, figures 1-6 of Lamatsch teach an integrated circuit comprising:

a clock distribution network (figure 1); and  
an inductance control circuit (figure 2, L)) coupled to the clock distribution network and receiving a clock signal (26), wherein the inductance control circuit, comprising an inductance, is configured to provide a resonant frequency of the clock distribution network as a function of a clock frequency of the clock signal.

10. With respect to claim 3, figure 2 of Lamatsch discloses the inductance provided by the inductance control circuit is fix.

11. With respect to claims 11 and 16, figure 2 of Lamatsch discloses a capacitance control circuit (C, CB) coupled between the clock distribution network and ground.

12. With respect to claims 18-20, the methods therein are inherent in the apparatus of Lamatsch as shown in the above rejections.

***Claim Rejections - 35 USC § 103***

13. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

14. Claim 4 is rejected under 35 U.S.C. 103(a) as being unpatentable over Cruz et al. (US 6,437,653) in view of New (US 5,999,025). The apparatus of Cruz does not specifically disclose the clock distribution network is used in a programmable logic device. Figure 6 of New discloses a clock distribution network used in a programmable logic device for the purpose of distributing a clock signal. It would have been obvious to one of ordinary skill of art at the time the invention was made to have used the clock distribution network of Cruz in a programmable logic device as taught by New for the purpose of distributing a clock signal.

***Allowable Subject Matter***

15. Claims 5-10, 12-15, 21 and 23-25 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

16. The following is an examiner's statement of reasons for allowance:

With respect to claims 5 and 12, the prior art does not teach a switchable bypass circuit.

With respect to claims 9 and 21, the prior art does not teach a second induction control circuit.

With respect to claim 23, the prior art does not teach a circuit having programmable inductance and programmable capacitance.

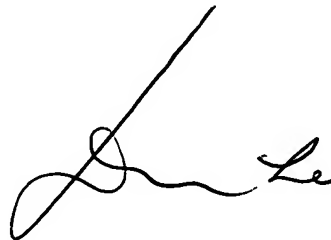
Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

17. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Don P Le whose telephone number is 571-272-1806. The examiner can normally be reached on 7AM - 5PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael J Tokar can be reached on 571-272-1812. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

9/16/2004

A handwritten signature in black ink, appearing to read "Don Le". The signature is fluid and cursive, with a large loop at the beginning and a distinct "Le" at the end.

**DON LE**  
**PRIMARY EXAMINER**